



<b>Title of Change:</b>	Datasheet Update for LC717A00AR-NH, LC717A00AJ-AH, LC717A10AR-NH and LC717A10AJ-AH.
<b>Effective date:</b>	29 September 2017
<b>Contact information:</b>	Contact your local ON Semiconductor Sales Office or <Tetsuya.Tokunaga@onsemi.com>
<b>Type of notification:</b>	This Product Bulletin is for notification purposes only. ON Semiconductor will proceed with implementation of this change upon publication of this Product Bulletin.
<b>Change category:</b>	<input type="checkbox"/> Wafer Fab Change <input type="checkbox"/> Assembly Change <input type="checkbox"/> Test Change <input checked="" type="checkbox"/> Other <u>Datasheet Update</u>

<b>Change Sub-Category(s):</b> <input type="checkbox"/> Manufacturing Site Change/Addition <input type="checkbox"/> Material Change <input checked="" type="checkbox"/> Datasheet/Product Doc change <input type="checkbox"/> Manufacturing Process Change <input type="checkbox"/> Product specific change <input type="checkbox"/> Shipping/Packaging/Marking <input type="checkbox"/> Other: _____	
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<b>Sites Affected:</b> <input type="checkbox"/> All site(s) <input checked="" type="checkbox"/> not applicable <input type="checkbox"/> ON Semiconductor site(s) : <input type="checkbox"/> External Foundry/Subcon site(s)
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**Description and Purpose:**

This PB is being issued in line with the Datasheet update for LC717A00AR-NH, LC717A00AJ-AH, LC717A10AR-NH and LC717A10AJ-AH due to documentation errors. Specific modifications are as follows:

Part Number	Page #	Description	From	To
<b>LC717A00AR-NH</b>	2	Remarks of nRST minimum pulse width in Electrical Characteristics	*1	Nothing
	2	Remarks of Power-on reset time in Electrical Characteristics	*1	Nothing
	3	Parameter words of VOL I2C in Electrical Characteristics	SDA pin leak current	SDA pin output voltage
	4	Unit of Data setup time in I2C Compatible Bus Timing Characteristics	μs	ns
	4	Unit of SDA, SCL rise/fall time in I2C Compatible Bus Timing Characteristics	μs	ns
<b>LC717A00AJ-AH</b>	2	Remarks of nRST minimum pulse width in Electrical Characteristics	*1	Nothing
	2	Remarks of Power-on reset time in Electrical Characteristics	*1	Nothing
	3	Parameter words of VOL I2C in Electrical Characteristics	SDA pin leak current	SDA pin output voltage
	4	Unit of Data setup time in I2C Compatible Bus Timing Characteristics	μs	ns
	4	Unit of SDA, SCL rise/fall time in I2C Compatible Bus Timing Characteristics	μs	ns



Part Number	Page #	Description	From	To
<b>LC717A10AR-NH</b>	2	Remarks of nRST minimum pulse width in Electrical Characteristics	*1	Nothing
	2	Remarks of Power-on reset time in Electrical Characteristics	*1	Nothing
	2	Note's description in Recommended Operating Conditions	*1: We recommend connecting large and small capacitance between VDD and VSS.  In this case, the small capacitance is equal to or more than 0.1 $\mu$ F, and layout nearby LSI.	Note 4: Inserting a high-valued capacitor and a low-valued capacitor in parallel between VDD and VSS is recommended.  In this case, the small-valued capacitor should be at least 0.1 $\mu$ F, and is mounted near the LSI.
	4	Unit of Data setup time in I2C Compatible Bus Timing Characteristics	$\mu$ s	ns
	4	Unit of SDA, SCL rise/fall time in I2C Compatible Bus Timing Characteristics	$\mu$ s	ns
	6	Description of I2C Compatible Bus Slave Address	Selection of two kinds of address is possible through the SA0 and SA1 terminals.	Selection of four kinds of address is possible through the SA0 and SA1 terminals.
<b>LC717A10AJ-AH</b>	2	Remarks of nRST minimum pulse width in Electrical Characteristics	*1	Nothing
	2	Remarks of Power-on reset time in Electrical Characteristics	*1	Nothing
	2	Note's description in Recommended Operating Conditions	*1: We recommend connecting large and small capacitance between VDD and VSS.  In this case, the small capacitance is equal to or more than 0.1 $\mu$ F, and layout nearby LSI.	Note 4: Inserting a high-valued capacitor and a low-valued capacitor in parallel between VDD and VSS is recommended.  In this case, the small-valued capacitor should be at least 0.1 $\mu$ F, and is mounted near the LSI.
	4	Unit of Data setup time in I2C Compatible Bus Timing Characteristics	$\mu$ s	ns
	4	Unit of SDA, SCL rise/fall time in I2C Compatible Bus Timing Characteristics	$\mu$ s	ns
	6	Description of I2C Compatible Bus Slave Address	Selection of two kinds of address is possible through the SA0 and SA1 terminals.	Selection of four kinds of address is possible through the SA0 and SA1 terminals.

There is no change to the product form, fit, and function

**List of affected Standard Parts:**

LC717A00AR-NH  
 LC717A00AJ-AH  
 LC717A10AR-NH  
 LC717A10AJ-AH