Notification Number: 20220			Date		August 09, 2022
Title: Datasheet for LMX2592 and LMX2582					
Customer Contact: PCN Manager Dept: Quality Services					
Change Type:					· · · · · · · · · · · · · · · · · · ·
Assembly Site		Design] [Wafer Bump Site
Assembly Process	$\overline{\boxtimes}$	Data Sheet			Wafer Bump Material
Assembly Materials		Part number change]	Wafer Bump Process
Mechanical Specification		Test Site]	Wafer Fab Site
Packing/Shipping/Labeling		Test Process]	Wafer Fab Materials
					Wafer Fab Process
	N	otification Details			
Description of Change:		<u> </u>			
Texas Instruments Incorporated is	ann	ouncing an information or	ılv no) ti	fication.
The product datasheet(s) is being i				,	nedelon.
The following change history provid					
•		arener decane.			
ŢEXAS					LMX2592
Instruments		SNAS	646G – I	DE	CEMBER 2015 – REVISED AUGUST 2022
Changes from Revision F (October 20)17)	to Revision G (August 2022)		Page
Changed package description from V					
Added a new requirement to Vtune p					
Added HD2 and HD3 information to the second se					
· Removed sentence: The CLK signal	shou	ıld not be high when LE transi	tions t	to	low8
 Changed the Channel Divider require 	emer	nt			16
 Added a new register field, VTUNE 					
 Changed the position of register field 					
 Added read only register R68, R69 a 					
Added additional requirement for reg					
Added additional information for regis					
Added a new register field, VTUNE_A Changed the register B0 FCAL LBE					
Changed the register R0 FCAL_LPF Changed the register R13 PED_CTI					
Changed the register R13 PFD_CTL position					
 Added External Loop Filter section					
Moved the Power Supply Recommendations and Layout sections to the Application and Implementation section					



Changes from Revision D (October 2017) to Revision E (August 2022)	Page
Changed package description from WQFN to VQFN	
Added a new requirement to Vtune pin description	
Removed sentence: The CLK signal should not be high when LE transitions to low	
Changed the Channel Divider requirement	
Added a new register field, VTUNE_ADJ, in register R30	
Changed the position of register field, PFD_CTL, in register R13	
Added read only register R68, R69 and R70	18
Added additional requirement for register CP_ICOARSE in Table 7-16	20
Added additional information for register MUXOUT_HDRV in Table 7-44	
Added a new register field, VTUNE_ADJ, in Table 7-25	
Changed the register R0 FCAL_LPFD_ADJ configurable values	
Changed the register R13 PFD_CTL position	20
Added the R68, R69 and R70 register field descriptions	
Added External Loop Filter section	
Moved the Power Supply Recommendations and Layout sections to the Application and Imple	
section	

The datasheet number will be changing.

Device Family	Change From:	Change To:
LMX2592	SNAS646F	SNAS646G
LMX2582	SNAS680D	SNAS680E

These changes may be reviewed at the datasheet links provided.

https://www.ti.com/product/LMX2592 https://www.ti.com/product/LMX2582

Error! Bookmark not defined.

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this notification:

None.

Product Affected:

LMX2592RHAR	LMX2592RHAT	LMX2582RHAR	LMX2582RHAT	

For questions regarding this notice, e-mails can be sent to the contact shown below or your local Field Sales Representative.

Location	E-Mail		
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