


PCN Number:	20230306001.1	PCN Date:	March 08, 2023
Title:	Datasheet for OPAx277		
Customer Contact:	PCN Manager	Dept:	Quality Services
Proposed 1st Ship Date:	June 8, 2023		
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
		<input type="checkbox"/>	Wafer Bump Site
		<input type="checkbox"/>	Wafer Bump Material
		<input type="checkbox"/>	Wafer Bump Process
		<input type="checkbox"/>	Wafer Fab Site
		<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Wafer Fab Process
Notification Details			
Description of Change:			
Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below.			
		OPA277, OPA2277, OPA4277 <small>SBOS079C – MARCH 1999 – REVISED FEBRUARY 2023</small>	

Changes from Revision B (April 2015) to Revision C (February 2023)

Page

- Updated the numbering format for tables, figures, and cross-references throughout the document..... 1
- Changed *Applications* bullets to include links..... 1
- Deleted text regarding identical specification for the single, dual, and quad versions..... 1
- Changed Offset Trim pin type from "Input" to "—"..... 3
- Changed "DFN" to "DRM (VSON)" in *OPA2277 Pin Functions* table..... 3
- Added table note for 10-mA current limit on input pins in *Absolute Maximum Ratings* 6
- Deleted operating temperature from *Absolute Maximum Ratings* 6
- Deleted lead temperature from *Absolute Maximum Ratings* 6
- Changed *Thermal Information* values for OPA2277 and OPA4277 SOIC packages..... 7
- Added test conditions to *Electrical Characteristics* header..... 8
- Changed format of *Electrical Characteristics* for readability..... 8
- Changed input offset voltage vs. time to long-term drift in *Electrical Characteristics* 8
- Changed input bias current test condition to separate over temperature specification..... 8
- Deleted redundant row in *open-loop gain* parameter..... 8
- Changed C_{LOAD} to C_L for consistency..... 8
- Changed Figure 6-14, *Change in Input Bias Current vs Common-Mode Voltage*, to correct typo in note..... 10
- Changed "DFN package" to "DRM package (8-pin VSON)"..... 21
- Changed "DFN package" to "DRM Package" and added "8-Pin VSON"..... 21
- Changed *Development Support* section to show updated links and resources..... 23

The datasheet number will be changing.

Device Family	Change From:	Change To:
OPAx277	SBOS079B	SBOS079C

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/OPA2277>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

Electrical specification performance changes as indicated above.

Changes to product identification resulting from this PCN:

None.

Product Affected:

OPA2277AIDRMT	OPA2277UA/2K5	OPA277PA	OPA277UAG4
OPA2277AIDRMTG4	OPA2277UA/2K5E4	OPA277PAG4	OPA277UG4
OPA2277P	OPA2277UAE4	OPA277U	OPA4277PA
OPA2277PA	OPA2277UAG4	OPA277U/2K5	OPA4277UA
OPA2277PAG4	OPA2277UG4	OPA277U/2K5G4	OPA4277UA/2K5
OPA2277U	OPA277AIDRMR	OPA277UA	OPA4277UA/2K5E4
OPA2277U/2K5	OPA277AIDRMT	OPA277UA/2K5	OPA4277UAE4
OPA2277U/2K5G4	OPA277AIDRMTG4	OPA277UA/2K5E4	OPA4277UAG4
OPA2277UA	OPA277P	OPA277UAE4	

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Location	E-Mail
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