

# NL17SG125

## Bus Buffer with 3-State Output

The NL17SG125 MiniGate™ is an advanced high-speed CMOS Bus Buffer with 3-State Output in ultra-small footprint.

The NL17SG125 input structures provides protection when voltages up to 4.6 V are applied.

### Features

- Wide Operating  $V_{CC}$  Range: 0.9 V to 3.6 V
- High Speed:  $t_{PD} = 2.4$  ns (Typ) at  $V_{CC} = 3.0$  V,  $C_L = 15$  pF
- Low Power Dissipation:  $I_{CC} = 0.5$   $\mu$ A (Max) at  $T_A = 25^\circ$ C
- 4.6 V Overvoltage Tolerant (OVT) Input Pins
- Ultra-Small Packages
- NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

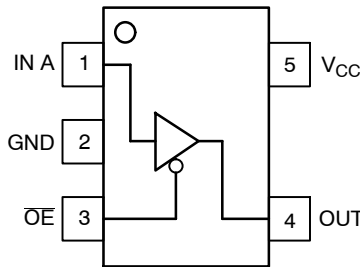


Figure 1. SOT-953 (Top Thru View)

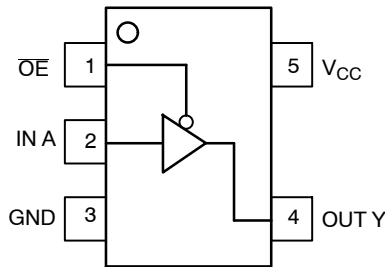


Figure 2. SC-88A (Top View)

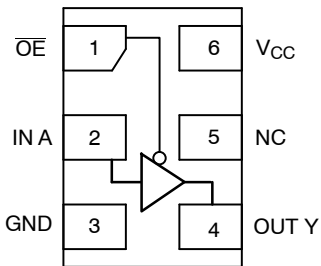


Figure 3. UDFN6 (Top View)



Figure 4. Logic Symbol

### PIN ASSIGNMENT

Pin Number	SOT-953	SC-88A	UDFN6
1	IN A	OE	OE
2	GND	IN A	IN A
3	OE	GND	GND
4	OUT Y	OUT Y	OUT Y
5	VCC	VCC	NC
6			VCC



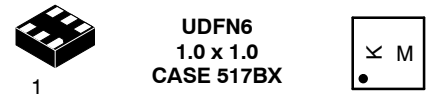
ON Semiconductor®

<http://onsemi.com>

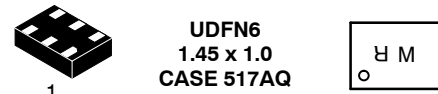
### MARKING DIAGRAMS



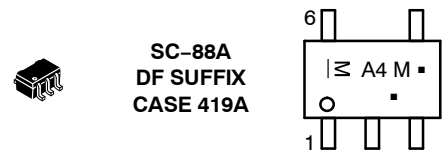
F = Specific Device Code  
(F with 90 degree clockwise rotation)  
M = Month Code



K = Specific Device Code  
(K with 270 degree clockwise rotation)  
M = Month Code



R = Specific Device Code  
(R with 180 degree clockwise rotation)  
M = Month Code



A4 = Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

### FUNCTION TABLE

A Input	OE Input	Y Output
L	L	L
H	L	H
X	H	Z

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 7 of this data sheet.

# NL17SG125

## MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
$V_{CC}$	DC Supply Voltage	-0.5 to +5.5	V
$V_{IN}$	DC Input Voltage	-0.5 to +4.6	V
$V_{OUT}$	DC Output Voltage Output at High or Low State Power-Down Mode ( $V_{CC} = 0$ V)	-0.5 to $V_{CC} + 0.5$ -0.5 to +4.6	V
$I_{IK}$	DC Input Diode Current $V_{IN} < GND$	-20	mA
$I_{OK}$	DC Output Diode Current $V_{OUT} < GND$	-20	mA
$I_{OUT}$	DC Output Source/Sink Current	$\pm 20$	mA
$I_{CC}$	DC Supply Current per Supply Pin	$\pm 20$	mA
$I_{GND}$	DC Ground Current per Ground Pin	$\pm 20$	mA
$T_{STG}$	Storage Temperature Range	-65 to +150	°C
$T_L$	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
$T_J$	Junction Temperature Under Bias	+150	°C
MSL	Moisture Sensitivity	Level 1	
$F_R$	Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
$V_{ESD}$	ESD Withstand Voltage Human Body Model (Note 2) Machine Model (Note 3)	>2000 >100	V
$I_{LATCHUP}$	Latchup Performance Above $V_{CC}$ and Below GND at 125°C (Note 4)	$\pm 100$	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2-ounce copper trace with no air flow.
2. Tested to EIA/JESD22-A114-A.
3. Tested to EIA/JESD22-A115-A.
4. Tested to EIA/JESD78.

## RECOMMENDED OPERATING CONDITIONS

Symbol	Characteristics	Min	Max	Unit
$V_{CC}$	Positive DC Supply Voltage	0.9	3.6	V
$V_{IN}$	Digital Input Voltage	0.0	3.6	V
$V_{OUT}$	Output Voltage Output at High or Low State Power-Down Mode ( $V_{CC} = 0$ V)	0.0 0.0	$V_{CC}$ 3.6	V
$T_A$	Operating Temperature Range	-55	+125	°C
$\Delta t / \Delta V$	Input Transition Rise or Fall Rate $V_{CC} = 3.3$ V $\pm$ 0.3 V	0	10	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

# NL17SG125

## DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C		T <sub>A</sub> = -55°C to +125°C		Unit
				Min	Max	Min	Max	
				V <sub>IH</sub>	High-Level Input Voltage		0.9	
			1.1 to 1.3	0.7xV <sub>CC</sub>		0.7xV <sub>CC</sub>		
			1.4 to 1.6	0.65xV <sub>CC</sub>		0.65xV <sub>CC</sub>		
			1.65 to 1.95	0.65xV <sub>CC</sub>		0.65xV <sub>CC</sub>		
			2.3 to 2.7	1.7		1.7		
			3.0 to 3.6	2.0		2.0		
V <sub>IL</sub>	Low-Level Input Voltage		0.9		GND		GND	V
			1.1 to 1.3		0.3xV <sub>CC</sub>		0.3xV <sub>CC</sub>	
			1.4 to 1.6		0.35xV <sub>CC</sub>		0.35xV <sub>CC</sub>	
			1.65 to 1.95		0.35xV <sub>CC</sub>		0.35xV <sub>CC</sub>	
			2.3 to 2.7		0.7		0.7	
			3.0 to 3.6		0.8		0.8	
V <sub>OH</sub>	High-Level Output Voltage	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -20 μA	0.9	0.75		0.75	V
			I <sub>OH</sub> = -0.3 mA	1.1 to 1.3	0.75xV <sub>CC</sub>		0.75xV <sub>CC</sub>	
			I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	0.75xV <sub>CC</sub>		0.75xV <sub>CC</sub>	
			I <sub>OH</sub> = -3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.45		V <sub>CC</sub> -0.45	
			I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0		2.0	
			I <sub>OH</sub> = -8.0 mA	3.0 to 3.6	2.48		2.48	
V <sub>OL</sub>	Low-Level Output Voltage	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 20 μA	0.9		0.1	0.1	V
			I <sub>OL</sub> = 0.3 mA	1.1 to 1.3		0.25xV <sub>CC</sub>	0.25xV <sub>CC</sub>	
			I <sub>OL</sub> = 1.7 mA	1.4 to 1.6		0.25xV <sub>CC</sub>	0.25xV <sub>CC</sub>	
			I <sub>OL</sub> = 3.0 mA	1.65 to 1.95		0.45	0.45	
			I <sub>OL</sub> = 4.0 mA	2.3 to 2.7		0.4	0.4	
			I <sub>OL</sub> = 8.0 mA	3.0 to 3.6		0.4	0.4	
I <sub>IN</sub>	Input Leakage Current	0 ≤ V <sub>IN</sub> ≤ 3.6 V	0 to 3.6			±0.1		μA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	3.6			1.0		μA
I <sub>OZ</sub>	3-State Output Leakage Current	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> V <sub>OUT</sub> = 0 to 3.6 V	0.9 to 3.6			1.0		μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

# NL17SG125

## AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3.0$ ns)

Symbol	Parameter	Test Condition	$V_{CC}$ (V)	$T_A = 25^\circ\text{C}$			$T_A = -55^\circ\text{C to } +125^\circ\text{C}$		Unit	
				Min	Typ	Max	Min	Max		
$t_{PLH}$ , $t_{PHL}$	Propagation Delay, A to Y	$C_L = 10$ pF, $R_L = 1$ M $\Omega$	0.9	-	11.3	13.6	-	15.9	ns	
			1.1 to 1.3	-	8.3	10.4	-	12.8		
			1.4 to 1.6	-	5.0	8.5	-	10.0		
			1.65 to 1.95	-	4.0	6.2	-	6.7		
			2.3 to 2.7	-	2.6	3.9	-	4.4		
			3.0 to 3.6	-	2.1	3.1	-	3.7		
		$C_L = 15$ pF, $R_L = 1$ M $\Omega$	0.9	-	12.6	14.7	-	17.0	ns	
			1.1 to 1.3	-	9.6	11.5	-	15.2		
			1.4 to 1.6	-	5.6	9.3	-	11.2		
			1.65 to 1.95	-	4.5	6.9	-	7.1		
			2.3 to 2.7	-	2.9	4.4	-	5.0		
			3.0 to 3.6	-	2.4	3.4	-	3.9		
		$C_L = 30$ pF, $R_L = 1$ M $\Omega$	0.9	-	14.5	16.3	-	19.6	ns	
			1.1 to 1.3	-	11.3	13.6	-	17.5		
			1.4 to 1.6	-	8.2	13.1	-	15.9		
			1.65 to 1.95	-	6	9.2	-	9.6		
			2.3 to 2.7	-	4	5.7	-	6.1		
			3.0 to 3.6	-	3.3	4.4	-	4.8		
$t_{PZH}$ , $t_{PZL}$	Output Enable Time, $\overline{OE}$ to Y	$C_L = 10$ pF; $R_L = 100$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$							ns	
			0.9	-	11.0	13.3	-	15.8		
			1.1 to 1.3	-	8.4	10.9	-	13.0		
			1.4 to 1.6	-	5.3	7.8	-	8.3		
			1.65 to 1.95	-	3.9	5.5	-	5.9		
			2.3 to 2.7	-	2.5	3.5	-	3.8		
			3.0 to 3.6	-	2.1	2.7	-	3		
		$C_L = 15$ pF; $R_L = 100$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$								ns
			0.9	-	12.0	14.8	-	17.0		
			1.1 to 1.3	-	9.0	11.7	-	13.8		
			1.4 to 1.6	-	5.9	8.9	-	11		
			1.65 to 1.95	-	4.4	6.3	-	6.5		
			2.3 to 2.7	-	2.9	3.9	-	4.2		
		$C_L = 30$ pF; $R_L = 100$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$								ns
			0.9	-	13.0	15.2	-	18.3		
			1.1 to 1.3	-	10.0	13.1	-	15.2		
			1.4 to 1.6	-	8.3	12.2	-	13.7		
			1.65 to 1.95	-	6.1	8.6	-	9.7		
			2.3 to 2.7	-	3.8	5	-	5.5		
		3.0 to 3.6	-	2.9	3.8	-	4.2			

# NL17SG125

## AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3.0$ ns) (continued)

Symbol	Parameter	Test Condition	$V_{CC}$ (V)	$T_A = 25^\circ\text{C}$			$T_A = -55^\circ\text{C to } +125^\circ\text{C}$		Unit	
				Min	Typ	Max	Min	Max		
$t_{PHZ}$ , $t_{PLZ}$	Output Disable Time, OE to Y	$C_L = 10$ pF; $R_L = 100$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$							ns	
			0.9	-	100.4	-	-	-		
			1.1 to 1.3	-	9.1	14.4	-	22.4		
			1.4 to 1.6	-	7.1	9.1	-	10.4		
			1.65 to 1.95	-	6.5	8.3	-	9		
			2.3 to 2.7	-	5.8	7.3	-	8.8		
			3.0 to 3.6	-	5.4	6.9	-	7.6		
		$C_L = 15$ pF; $R_L = 100$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$								ns
			0.9	-	122.2	-	-	-		
			1.1 to 1.3	-	9.8	15.3	-	25.1		
			1.4 to 1.6	-	7.8	9.8	-	11.3		
			1.65 to 1.95	-	7.2	9.2	-	10.6		
			2.3 to 2.7	-	7	8.2	-	10.3		
			3.0 to 3.6	-	6.6	7.7	-	9.5		
		$C_L = 30$ pF; $R_L = 100$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$ $R_L = 5$ k $\Omega$								ns
			0.9	-	217.1	-	-	-		
			1.1 to 1.3	-	13.2	19.6	-	31.9		
			1.4 to 1.6	-	12.2	13.5	-	14.9		
			1.65 to 1.95	-	11.4	12.7	-	13.9		
			2.3 to 2.7	-	11.3	12.2	-	13.5		
			3.0 to 3.6	-	10.2	11.5	-	12.9		
$C_{IN}$	Input Capacitance		0 to 3.6		3	-	-	-	pF	
$C_O$	Output Capacitance	$V_O = \text{GND}$	0		3	-	-	-	pF	
$C_{PD}$	Power Dissipation Capacitance (Note 5)	$f = 10$ MHz	0.9 to 3.6	-	4	-	-	-	pF	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5.  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation:  $I_{CC(OPR)} = C_{PD} \cdot V_{CC} \cdot f_{in} + I_{CC}$ .  $C_{PD}$  is used to determine the no-load dynamic power consumption;  $P_D = C_{PD} \cdot V_{CC}^2 \cdot f_{in} + I_{CC} \cdot V_{CC}$ .

# NL17SG125

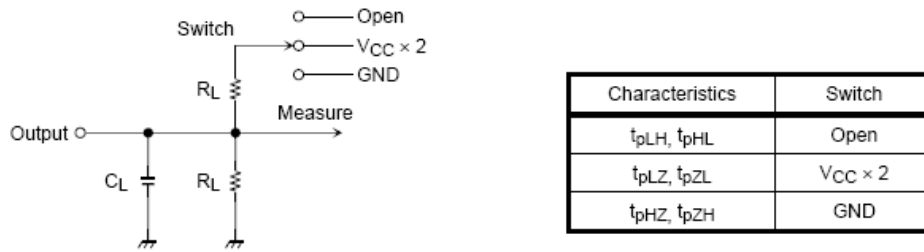


Figure 5. Test Circuit

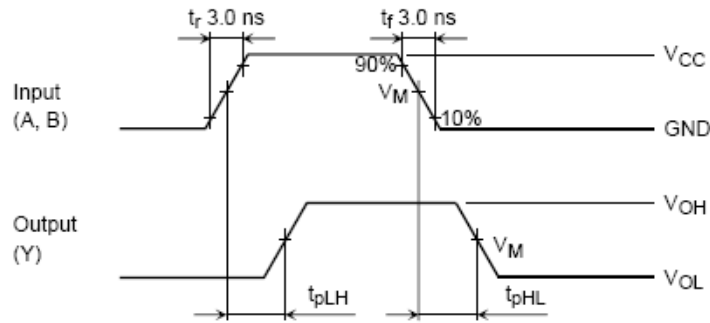


Figure 6.  $t_{pLH}$ ,  $t_{pHL}$  Waveforms

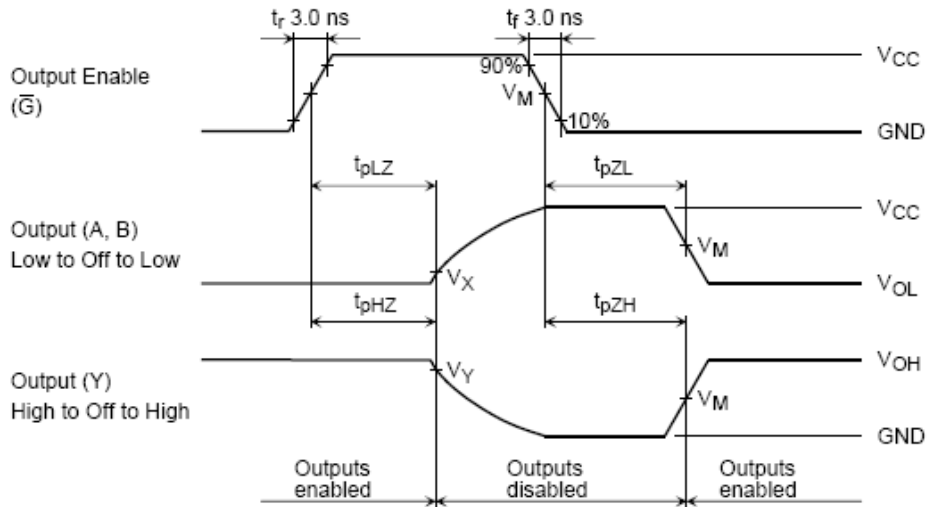


Figure 7.  $t_{pLZ}$ ,  $t_{pHZ}$ ,  $t_{pZH}$ ,  $t_{pZL}$  Waveforms

# NL17SG125

## ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
NL17SG125P5T5G	SOT-953 (Pb-Free)	8000 / Tape & Reel
NL17SG125DFT2G	SC-88A (Pb-Free)	3000 / Tape & Reel
NLV17SG125DFT2G*	SC-88A (Pb-Free)	3000 / Tape & Reel
NL17SG125MU1TCG**	UDFN6 1.45 x 1 mm (Pb-Free)	3000 / Tape & Reel
NL17SG125MU3TCG**	UDFN6 1 x 1 mm (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

\*\*In Development

# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



SCALE 2:1

## SC-88A (SC-70-5/SOT-353) CASE 419A-02 ISSUE M

DATE 11 APR 2023



RECOMMENDED  
MOUNTING FOOTPRINT

\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. 419A-01 OBSOLETE. NEW STANDARD 419A-02
4. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.1016MM PER SIDE.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.80	0.95	1.10
A1	---	---	0.10
A3	0.20 REF		
b	0.10	0.20	0.30
c	0.10	---	0.25
D	1.80	2.00	2.20
E	2.00	2.10	2.20
E1	1.15	1.25	1.35
e	0.65 BSC		
L	0.10	0.15	0.30

### GENERIC MARKING DIAGRAM\*



\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

XXX = Specific Device Code

M = Date Code

▪ = Pb-Free Package

(Note: Microdot may be in either location)

#### STYLE 1:

1. BASE
2. EMITTER
3. BASE
4. COLLECTOR
5. COLLECTOR

#### STYLE 2:

1. ANODE
2. EMITTER
3. BASE
4. COLLECTOR
5. CATHODE

#### STYLE 3:

1. ANODE 1
2. N/C
3. ANODE 2
4. CATHODE 2
5. CATHODE 1

#### STYLE 4:

1. SOURCE 1
2. DRAIN 1/2
3. SOURCE 1
4. GATE 1
5. GATE 2

#### STYLE 5:

1. CATHODE
2. COMMON ANODE
3. CATHODE 2
4. CATHODE 3
5. CATHODE 4

#### STYLE 6:

1. EMITTER 2
2. BASE 2
3. EMITTER 1
4. COLLECTOR
5. COLLECTOR 2/BASE 1

#### STYLE 7:

1. BASE
2. EMITTER
3. BASE
4. COLLECTOR
5. COLLECTOR

#### STYLE 8:

1. CATHODE
2. COLLECTOR
3. N/C
4. BASE
5. EMITTER

#### STYLE 9:

1. ANODE
2. CATHODE
3. ANODE
4. ANODE
5. ANODE

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

<b>DOCUMENT NUMBER:</b>	<b>98ASB42984B</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>SC-88A (SC-70-5/SOT-353)</b>	<b>PAGE 1 OF 1</b>

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.



# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

ON Semiconductor®



SCALE 4:1

**SOT-953**  
CASE 527AE  
ISSUE E

DATE 02 AUG 2011



TOP VIEW



SIDE VIEW



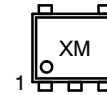
BOTTOM VIEW

**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	0.34	0.37	0.40
b	0.10	0.15	0.20
C	0.07	0.12	0.17
D	0.95	1.00	1.05
E	0.75	0.80	0.85
e	0.35 BSC		
H <sub>E</sub>	0.95	1.00	1.05
L	0.175 REF		
L2	0.05	0.10	0.15
L3	---	---	0.15

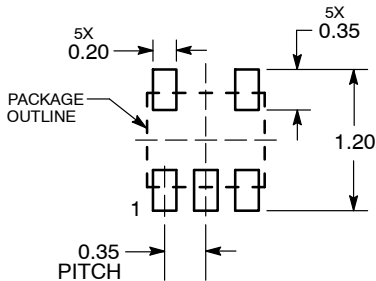
**GENERIC MARKING DIAGRAM\***



X = Specific Device Code  
M = Month Code

\*This information is generic. Please refer to device data sheet for actual part marking.  
Pb-Free indicator, "G" or microdot "▪", may or may not be present.

**SOLDERING FOOTPRINT\***



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

<b>DOCUMENT NUMBER:</b>	<b>98AON26457D</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>SOT-953</b>	<b>PAGE 1 OF 1</b>

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

### LITERATURE FULFILLMENT:

Email Requests to: [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**onsemi Website:** [www.onsemi.com](http://www.onsemi.com)

### TECHNICAL SUPPORT

**North American Technical Support:**

Voice Mail: 1 800-282-9855 Toll Free USA/Canada

Phone: 011 421 33 790 2910

**Europe, Middle East and Africa Technical Support:**

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative